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U.S. UTILITY Patent Application

10	PL NUM 090978	FILING DATE 03/04/2002	CLASS 438	SUBCLASS 20	GAU,	EXAMINER WILLE
**AF	PLICANT	S: Pio Fed	lerico; Zul	iani Paola;		
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ITLE : f	ield-effec	t transistor and cor	respondin	g manufacturir	ng method	
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NOTICE OF ALLOWANCE MAILED			CLAIMS ALLOWED			
	**	Assistant Examiner	Total Claims		Print Claim for O.G	
ISSUE FEE Amount Due Date Paid			DRAWING			
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TERMINAL DISCLAMER		Primary Examiner	Application Examiner			
		PREPARED FOR ISSUE				
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